EE476C_LiPoCrew_IssueTracker_1

Dataforth - LiPo Crew

Project manager	Jace Jenkins
Project dates	Sep 21, 2020 - Apr 10, 2021
Completion	61%
Tasks	61
Resources	4

http://

Name	Begin date	End date	
Research: Lit Review	9/21/20	10/2/20	
Requirements	9/29/20	10/14/20	
Requirments Document	9/29/20	10/12/20	
Requirements Finished	10/12/20	10/12/20	
Architecture Draft#1	10/6/20	10/13/20	
Architecture Final Draft	10/14/20	10/14/20	
Design Analysis	10/15/20	11/27/20	
Lvel 2 architecture	10/15/20	11/13/20	
Prelimary Software/Coding	10/26/20	11/27/20	
Preliminary Circuit Design	10/26/20	11/27/20	
IND. Prototypes	10/30/20	11/9/20	
Full Desing layout	11/25/20	11/25/20	
Team Design presentation	11/2/20	11/13/20	
Winter Break	11/25/20	12/8/20	
Prototyping/Control Testing	12/8/20	3/26/21	
Proto Stage 1	12/8/20	1/8/21	
Circuit Design	12/8/20	12/25/20	
Software/Coding	12/8/20	12/25/20	
Integrated Solution	12/9/20	12/11/20	
Integrated Level 3 Architecture	12/9/20	12/11/20	
Circuit Analysis	12/9/20	12/11/20	
Discrete Solution	12/9/20	1/8/21	
Discrete Level 2 Architecure	12/9/20	12/9/20	
Discrete Level 3 Architecture	12/9/20	12/9/20	
Circuit Analysis	12/10/20	12/17/20	

Tasks

lame	Begin date	End date	
Circuit Simulation	12/18/20	1/1/21	
ORDER PARTS	1/4/21	1/8/21	
Christmas Break	12/25/20	12/28/20	
Proto Stage 2	1/18/21	2/23/21	
Software/Coding	1/18/21	2/23/21	
ADC	1/18/21	1/25/21	
Cell Balancing Alg	1/26/21	2/8/21	
Thermocouples	2/9/21	2/15/21	
Interrupts/Safety	2/16/21	2/19/21	
Functional Software Prototype	2/24/21	2/24/21	
Final Circuit design & Sim	1/18/21	2/12/21	
Cell Charging	1/18/21	1/25/21	
Battery Protection	1/18/21	1/25/21	
Dual 5v rail and connections	1/26/21	2/2/21	
Progress/Status presentaion	2/3/21	2/12/21	
Functional simulated prototype	2/22/21	2/22/21	
Final: Stage 3	2/1/21	3/19/21	
Vector Board/Physical Prototype	2/1/21	3/5/21	
Testing Presentation	3/5/21	3/5/21	
Full hardware system integration	3/5/21	3/5/21	
Testing	2/26/21	3/19/21	
PCB	2/26/21	3/8/21	
Unit step-by-step test	3/10/21	3/10/21	
Unit matrix test	3/11/21	3/12/21	
CC/CV	3/11/21	3/12/21	
Integration Test	3/17/21	3/19/21	
Complete Integration	3/17/21	3/26/21	
Full Product Cover	3/17/21	3/23/21	
PCB Complete Integration	3/17/21	3/26/21	

Tasks

Name	Begin date	End date	
Test Results Presentation	3/26/21	3/26/21	
Documentation	1/1/21	4/9/21	
Website	1/1/21	3/19/21	
Abstract	3/1/21	3/19/21	
Project Poster	3/10/21	3/31/21	
Video Demonstration	3/25/21	3/31/21	
User Manual	3/1/21	4/9/21	

Resources

Name	Default role
Jonathan Ciulla	no specific role
Jace Jenkins	project manager
Mana Santiago	no specific role
Luis Camargo	no specific role

EE476C_LiPoCrew_lssueTracker_1

Gantt Chart

GANTT.	\leftarrow	>	2020		Ci	rcuit Simulat	ion I	2021							Function	al similated	prototype	system inter	aration										
project			Week 49	Week 50	Week 51	Week 52	Week 1	Week 2	Week 3	Week 4	Week 5	Week 6	Wask 7	Week 8	Week 9	Week 10	Week 11	Week 12	Week 13	Week 14	Week 15	Week 16	Week 17	Week 18	Week 19	Week 20	Week 21	Week 22	Week 23
Name	Begin date	End date	11/29/20	12/6/20	12/13/20	12/20/20	12/27/20	1/3/21	1/10/21	1/17/21	1/24/21	1/31/21	2/7/21	2/14/21	2/21/21	2/28/21	3/7/21	3/14/21	3/21/21	3/28/21	4/4/21	4/11/21	4/18/21	4/25/21	5/2/21	5/9/21	5/16/21	5/23/21	5/30/21
Research: Lit Review	9/21/20	10/2/20	1												_													-	_
Requirements	9/29/20	10/14/20	-																										-
 Requirments Document 	9/29/20	10/12/20																										-	_
 Requirements Finished 	10/12/20	10/12/20	-		_	-			-		-	-			_				-							-		-	_
Architecture Draft#1	10/6/20	10/13/20	1	_		-	_	_	-		-	-	_		_			-	-	-	-		-	-		-	-	-	_
Architecture Final Draft	10/14/20	10/14/20		-	-	-	-		-	-	-	-			-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Design Analysis	10/15/20	11/27/20	-																										
Lvel 2 architecture	10/15/20	11/13/20 11/27/20	1																										
 Prelimary Software/Coding Preliminary Circuit Design 		11/27/20	1			-			-		-	-				-	-	-	-	-	-		-	-		-	-	-	_
 IND. Prototypes 	10/20/20	11/2//20				-						-	_					-	-			_				-	-	-	
 Full Desing layout 	11/25/20	11/25/20		-			-				-						-	-					-	-				-	-
Team Design presentation		11/13/20	-	_			_												-	-	-		-			-		-	-
Winter Break	11/25/20	12/8/20		*****		-													-									-	_
Prototyping/Control Testing	12/8/20	3/26/21									-																	-	-
Prototyping/Control resting Proto Stage 1	12/8/20	1/8/21																			-		-	-		-		-	_
Circuit Design			-	_		_					-	-			_	_	-	-	-	-	-		-	-		-		-	_
Software/Coding	12/8/20 12/8/20	12/25/20 12/25/20	-																										
 Software/Coding Integrated Solution 	12/8/20	12/11/20	-																										
 Integrated Solution Integrated Level 3 		12/11/20	-																							-			
Circuit Analysis	12/9/20	12/11/20	-																			-							
 Discrete Solution 	12/9/20	1/8/21		4													-	-	-	-			-					-	-
Discrete Joidtion Discrete Level 2 Ar		12/9/20	-	-																				-					-
Discrete Level 2 Ar Discrete Level 3 Ar		12/9/20				-			-			-						-	-	-	-			-		-	-	-	-
Circuit Analysis	12/10/20	12/17/20	-	- 7	_																							-	-
Circuit Analysis Circuit Simulation		1/1/21		_		_	_				-	-	-						-	-	-	-	-	-		-		-	-
ORDER PARTS	1/4/21	1/8/21						· •																					
Christmas Break	12/25/20	12/28/20				E	1111	_																					
 Proto Stage 2 	1/18/21	2/23/21		-						-	-	-	-	-	_													-	-
 Software/Coding 	1/18/21	2/23/21									_	_		_				-	-				-						_
ADC	1/18/21	1/25/21																		-								-	_
 Cell Balancing Alg 		2/8/21										_																	
Thermocouples	2/9/21	2/15/21																											
 Interrupts/Safety 		2/19/21																											
 Functional Softwa. 		2/24/21													٠														
Final Circuit design &		2/12/21								-	-		_																
 Cell Charging 	1/18/21	1/25/21																											
 Battery Protection 	1/18/21	1/25/21																											
 Dual 5v rail and c 	1/26/21	2/2/21										1																	
 Progress/Status pr 		2/12/21																											
 Functional simulated prot. 	2/22/21	2/22/21													•														
 Final: Stage 3 	2/1/21	3/19/21																											
 Vector Board/Physical 		3/5/21																											
 Testing Presentation 		3/5/21																											
 Full hardware system i 		3/5/21														•													
E • Testing	2/26/21	3/19/21	_																										
PCB	2/26/21	3/8/21																				_							
 Unit step-by-step 		3/10/21																											
 Unit matrix test 	3/11/21	3/12/21															L.												
 CC/CV 	3/11/21	3/12/21	_															_											
 Integration Test 	3/17/21	3/19/21	-						-				-		_	-				_							-		-
 Complete Integration 	3/17/21	3/26/21	_																	•		-						-	
 Full Product Cover 	3/17/21	3/23/21																										-	
 PCB Complete Integra 		3/26/21	-																									-	
 Test Results Presentation 		3/26/21	-					_	_	_	_			_	_	_	_	_	-		_			-		-			
Documentation	1/1/21	4/9/21	-				-																						
Website	1/1/21	3/19/21									-			_		_		1.1				_							
Abstract	3/1/21	3/19/21	-														_	11				_							
Project Poster	3/10/21	3/31/21	-															-	-										
Video Demonstration	3/25/21	3/31/21	-						-		-		-		_	_	_	-	_		_			-		-	-	-	-
 User Manual 	3/1/21	4/9/21															_	-			1								

7

Resources Chart

GANTT	2020					2021	_						Eurictional simulated prototype persystem integration														
Name	Default role	Week 50	Week 51	Week 52	Week 1	Week 2	Week 3	Week 4	Week 5	Week 6	Week 7	Week 8	Week 9	Week 10	Week 11	Week 12	Week 13	Week 14	Week 15	Week 16	Week 17	Week 18	Week 19	Week 20	Week 21	Week 22	Week 23
🛨 🏾 Jonathan Ciulla	no specific r			200%	200%	200%																120021	GL/L1	0.021	0.1021	0120021	COURT 1
 Jace Jenkins 	project man		200%	200%	200%	200%						1															
\pm 🍳 Mana Santiago	no specific r					200%		200%	200%	200%	200%			200%	200%	200%											
\pm 🍳 Luis Camargo	no specific r																										